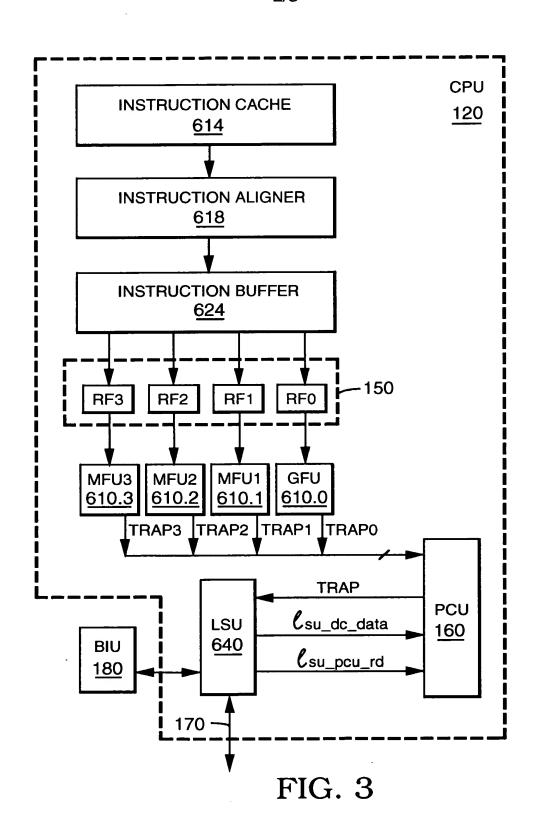


F A D/R E C(A1) A2 A3 T WB F A D/R E C(A1) A2 A3 T WB

FIG. 2



3/3

LSU 640 LOAD BUF 720 720RD 720RAW 720S -720T 720A addrb **RAW** 4 stage trap_taken 3 2 1 0 STORE BUF 710 710A -710D -710S - 710L 710Taddrb datab stage 7 6 0 BIU LIST 730 3 2 1 0

FIG. 4

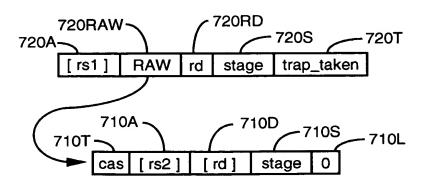


FIG. 5